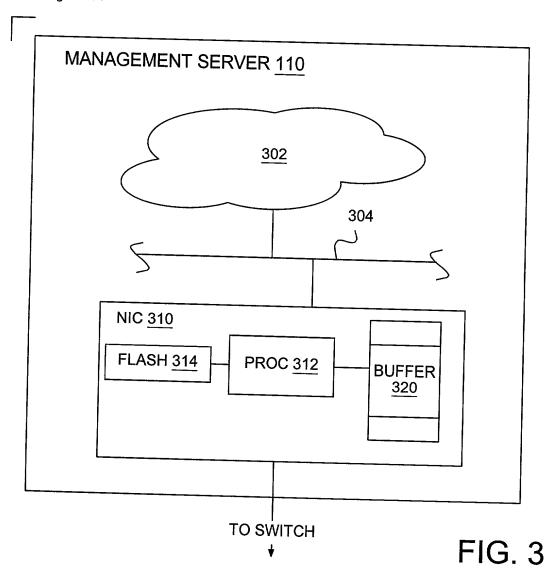


FIG. 1

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> **PROCESSOR PROCESSOR** <u>202</u> <u>202</u> 206 **MEMORY** <u>204</u> 208 **BUS BRIDGE** <u>209</u> DISK I/O 1/0 NIC 210 <u>222</u> 101 <u>224</u>

FIG. 2



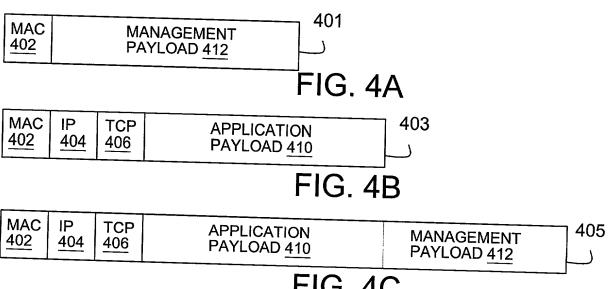


FIG. 4C

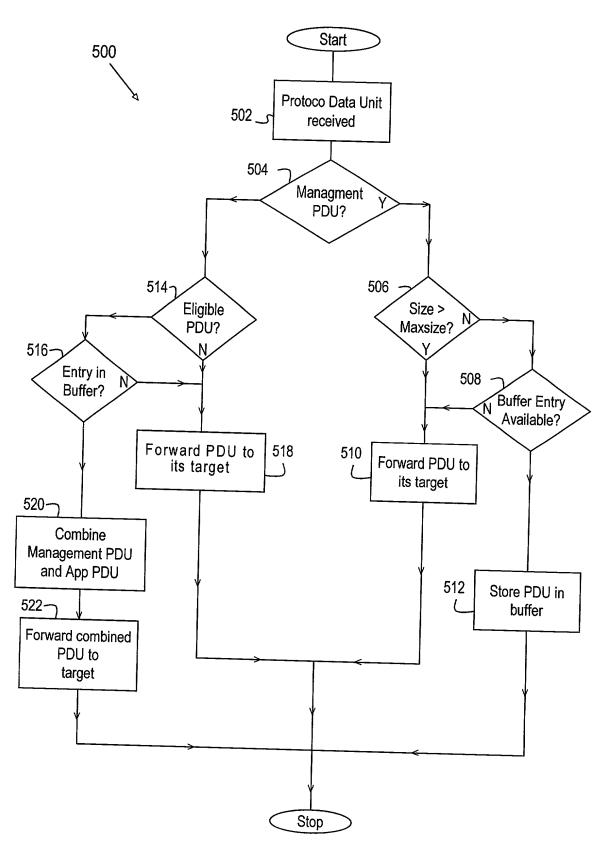


FIG. 5

Fig

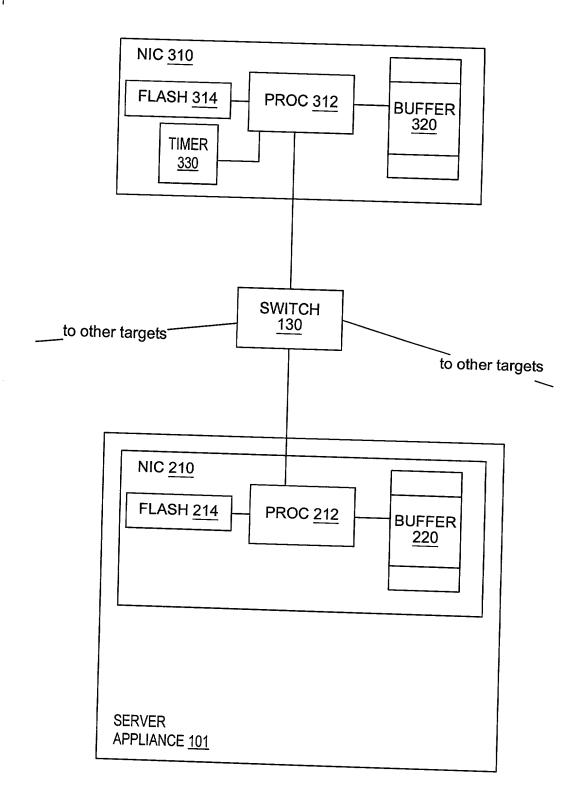


FIG. 6

The Will Man

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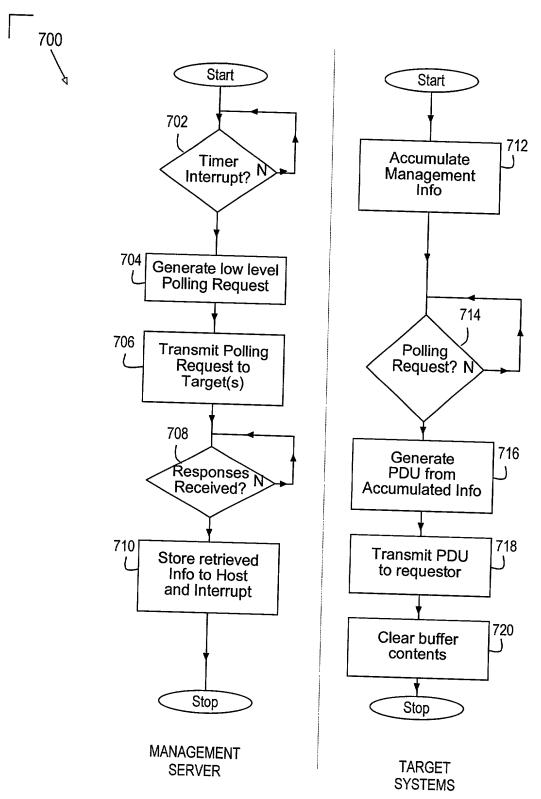
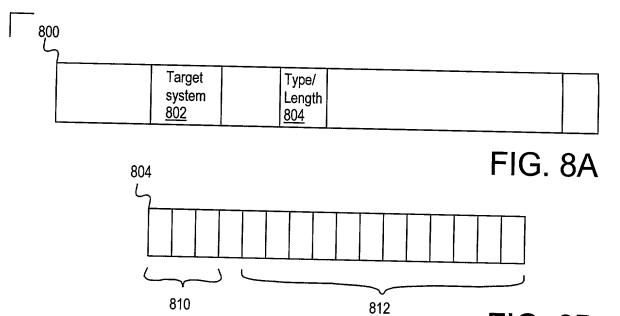


FIG. 7



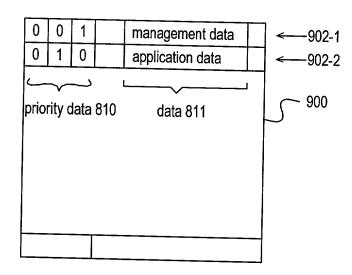


FIG. 9

FIG. 8B

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